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NORTH AMERICA INTELLECTUAL PROPERTY CORPORATION			EXAMINER	
P.O. BOX 506			SINGH, HIRDEPAL	
MERRIFIELD, VA 22116				
			ART UNIT	PAPER NUMBER
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Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

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Office Action Summary

Application No.

10/710,262

Applicant(s)

KAO ET AL.

Examiner

Hirdepal Singh

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 07 September 2007.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-20 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- ☐ Notice of References Cited (PTO-892)
- ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- ☐ Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date _____
- ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date _____
- ☐ Notice of Informal Patent Application
- ☐ Other: _____

DETAILED ACTION

Response to Arguments

1. Applicant's amendment regarding the objection to the specification has corrected the informalities. Therefore, the objection to the specification is withdrawn.
2. The Amendment corrected the informalities in the claims 7, 8, 17 and 18. Therefore, objection to these claims is withdrawn.
3. Applicant's arguments filed September 07, 2007 have been fully considered but they are not persuasive.
4. Applicant argues "...the patentable differences between claims 1 and 11 and the cited prior art references. Claim 1 has been amended to state the step of "down-converting the primary digital signal into a second data rate lower than the basic data rate and then processing the primary digital signal according to the second data rate to detect whether the primary digital signal with the second data rate carries information of a second predetermined mode...Each of the claims states that the primary digital signal having the basic data rate is first down-converted into the primary digital signal into a second data rate lower than the basic data rate. Next, the primary digital signal with the second data rate is processed to detect whether the primary digital signal with the second data rate carries information of a second predetermined mode. On the other hand, Teo teaches in Figure 2 and in column 10, lines 21-32, decoding a

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voice signal with a decoder 40 before using the rate converter 42 to convert the frequency of the voice signal. Therefore, Teo does not teach the claimed limitations of first down-converting the primary digital signal having the basic data rate into the primary digital signal into a second data rate lower than the basic data rate, and then processing the primary digital signal with the second data rate to detect whether the primary digital signal with the second data rate carries information of a second predetermined mode."

5. Examiner's response: The Applicant's opinion that Teo does not disclose "first down-converting the primary digital signal...and then processing the ..." is not persuasive. The Applicant pointed out that figure 2 of Teo reference doesn't have the claimed feature. The Applicant(s) is/are reminded that the rejection is made based on the entire content of the cited prior art. Figure 3 of Teo clearly shows the claimed feature i.e. the primary digital signal first goes to rate converter and then goes to decoder for further processing. Here, it is to be noted that figure 3 is another embodiment of the invention where the channelizers 32 are provided with rate converter 50 that adjust the sampling rate from 50 fc to a reduced multiple of the required rates for the voice and data signals. In the case of a voice signal at 1XRTT (8 fc) and a data signal at UTRAN (2fc.sub.(UTRAN), i.e. 6.25 fc), the reduced multiple rate may be 25 fc (i.e. 8 times the chip rate for UTRAN) or other rate as may be convenient for rate adjustment. By adjusting the sampling rate downward at the channelizers 32, the computational requirements at the signal handling devices 34 can be reduced (column 11, lines 45-67).

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6. Therefore, the rejection to independent Claims 1 and 11 and the dependent claims thereof remains unchanged as the cited reference(s) teach all the limitations.

Claim Rejections - 35 USC § 103

7. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

8. Claims 1, 4, 5, 7, 9, 11, 14, 15, and 17 are rejected under 35 U.S.C. 103(a) as being unpatentable over Teo et al. (US 6,985,545) in view of Tsuie et al. (US 2004/0223449).

Regarding Claims 1, and 11:

Teo discloses a method and wireless receiver apparatus capable of handling signals of different modes i.e. different air interface standards by same or common analog to digital converter to digitize the signals at a common rate (abstract) comprising;
receiving a radio frequency RF signal through antenna (abstract; column 6, lines 44-52; figure 1);
down converting the received RF signal to base band signal (column 6, lines 60-63);

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converting the base band signal to digital signal with analog to digital converter with a data or sample or digitizing rate (column 2, lines 14-25; column 6, lines 55-67);

down converting the primary signal into second data rate lower than basic data rate then processing the signal (figure 3; column 11, lines 45-67) according to a second data rate i.e. down converted in the rate converter to detect the information of a second predetermined mode (column 2, lines 60-67-column3, lines 1-3).

Teo discloses all of the subject matter as described above and further discloses that processing the digital signal in the signal handling device and adjusting the data rate (column 2, lines 55-64) as required, except for specifically teaching that the digital signal is processed according to first data rate to detect if the signal is in a first predetermined mode.

However, Tsuie discloses a similar receiver and method capable of handling signals of different modes (abstract), and detecting the transmission mode of the received signal (paragraphs 0048-0049; figure 3).

Therefore, it would have been obvious to one of ordinary skill in the art at the time of invention to process the signal according to first data rate for detecting whether the digitized signal is in a first predetermined transmission mode. One would have been motivated to detect if the signal carries information of a predetermined first mode to avoid the data rate conversion, which would be necessary for one of the receiving modes, to reduce the complexity and cost of the receiving device.

Regarding Claims 4, and 14:

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Teo discloses all of the subject matter as described above, and further discloses that the first data rate i.e. standard digital signal processing rate and basic data rate i.e. common digitizing rate are multiples, except for specifically teaching that the first data rate and basic data rate are same.

However, it would have been obvious to one having ordinary skill in the art at the time the invention was made that if the primary signal i.e. digitized signal has a data rate, which is according to Nyquist theorem, twice the highest bandwidth signal then the first data rate i.e. the data rate of the highest frequency is same as the sampled rate or primary rate.

Regarding Claims 5, and 15:

Teo discloses all of the subject matter as described above, and further discloses that the first data rate is a multiple of a standard rate i.e. basic sampling rate (column 4, lines 54-67).

Regarding Claims 7, and 17:

Teo discloses all of the subject matter as described above, and further discloses that the predetermined modes could be CDMA, TDMA, GSM (column 6, lines 10-16; column 8, lines 40-55), but doesn't specifically say that the modes are GSM-1800 and WCDMA.

However, it would have been obvious to one having ordinary skill in the art at the time the invention was made to make use of different modes as GSM-1800 and

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WCDMA for transmitting the signal. One would have been motivated to implement these modes to be used in the system to achieve high speed and to catch up with the next generation technology.

Regarding Claim 9:

Teo discloses all of the subject matter as described above, and further discloses that sampling the signal at a first data rate which is a multiple of a standard rate i.e. basic sampling rate is a multiple of a first data rate (column 4, lines 54-67), but doesn't explicitly disclose that the basic data rate is multiple of lower between a first and a second data rate.

However, it would have been obvious to one having ordinary skill in the art at the time the invention was made to sample the signal at a rate multiple of lower between the two data rates of different standards in order to avoid using an extra down converter in the receiver, and the signal with higher data rate is down converted via a rate converter when that signal mode is less prone to noise.

9. Claims 6, 8, 16, 18, and 19 are rejected under 35 U.S.C. 103(a) as being unpatentable over Teo et al. (US 6,985,545) in view of Tsuie et al. (US 2004/0223449), as in claims 1 and 11 above, and in further view of Womack et al. (US 5,982,819).

Regarding Claim 6:

Teo discloses all of the subject matter as described above except for specifically teaching that the primary signal is down converted when the first data rate is lower than the basic data rate.

However, Womack discloses a similar system and method for wireless receiver capable of handling signals of different modes and the digital signal is processed and down converted to basic data rate in digital down converters (column 5, lines 22-51) it is inherent that the signal is down converted when the first data rate is lower than the basic data rate i.e. the signal is over sampled.

Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to process the signal according to first data rate and down converting to first data rate. One would have been motivated to use the Womack receiver down converter to bring the basic data rate to first data rate according to the predetermined mode.

Regarding Claim 16:

Teo discloses all of the subject matter as described above except for specifically teaching that the primary signal is down converted when the second data rate is lower than the first data rate.

However, Womack discloses a similar system and method for wireless receiver capable of handling signals of different modes and the digital signal is processed and down converted to basic data rate in digital down converters (column 5, lines 22-51).

Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to down convert the signal to the first data rate when second data rate is lower than the first data rate as the digitized signal is multiple of the lower data rate, and required to be down converted via a rate converter to the higher data rate mode. One would have been motivated to use the Womack receiver down converter to bring the basic data rate in Teo system to first data rate i.e. the higher data rate according to the predetermined mode.

Regarding Claims 8, and 18:

Teo discloses all of the subject matter as described above except for specifically teaching that predetermined modes could be orthogonal frequency division multiplexing OFDM and second mode is direct sequence spread spectrum DSSS.

However, Tsuie discloses that the predetermined modes could be orthogonal frequency division multiplexing OFDM (abstract; paragraph 0057). Womack discloses a similar system and method for wireless receiver capable of handling signals of different modes, and further discloses that the modes or formats could be frequency shift keyed FSK or direct sequence spread spectrum DSSS (column 2, lines 25-40).

Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to make use of different modes as direct sequence spread spectrum DSSS/CCK, orthogonal frequency division multiplexing OFDM for transmitting the signal. One would have been motivated to implement these modes to

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be used in the Teo system to achieve high speed and to catch up with the users of the next generation technology.

Regarding Claim 19:

Teo discloses all of the subject matter as described above except for specifically teaching that the rate converter i.e. digital down converter is a farrow interpolator or a Decimation filter.

However, Womack discloses a similar system and method for wireless receiver capable of handling signals of different modes, and further discloses that the digital down converter i.e. rate converter is a decimation filter (column 5, lines 25-40).

Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to implement the decimation filter as a rate converter in the Teo system. One would have been motivated to implement the decimation filter rate converter to decimate the data rate down to the predetermined mode.

10. Claims 2, 3, 12, and 13 are rejected under 35 U.S.C. 103(a) as being unpatentable over Teo et al. (US 6,985,545) in view of Tsuie et al. (US 2004/0223449) as applied to claims 1 and 11 above, and further in view of Karaoguz (US 2004/0029620).

Regarding Claims 2, 3, and 12:

Teo discloses all of the subject matter as described above except for specifically teaching a power control module for switching one base band processing modules into a power saving mode when other base band processing module detects that the primary signal is in corresponding predetermined mode.

However, Karaoguz discloses a similar method and device receiving signals of different communication standards or modes (abstract; paragraph 0005), and further discloses a power control module temporarily switching base band processing module to power up and power down modes (figures 2, 6; paragraph 0043).

Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to use the power control module for temporarily switching one of the base band processing modules to power saving mode corresponding to the primary signal's predetermined mode. One would have been motivated to combine the power control module in Teo system in order to extend the battery life and get better power management for the RF receiver.

Regarding Claim 13:

Teo discloses all of the subject matter as described above except for specifically teaching to power up the elements of radio receiver for known communication periods.

Karaoguz discloses power up the elements of radio receiver for known communication periods (abstract; paragraph 0011).

Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to use the power control module for switching the base

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band processing modules back up to full power mode corresponding to the transmission procedures. One would have been motivated to use the power control module in Teo system to put base band processing modules back up in full power mode corresponding to the transmission procedures in order to enable the receiver to engage in communication, and get better power management.

11. Claim 10 is rejected under 35 U.S.C. 103(a) as being unpatentable over Teo et al. (US 6,985,545) in view of Tsue et al. (US 2004/0223449) as applied to claim 1 above, and further in view of Spiegel et al. (US 7,161,997).

Regarding Claim 10:

Teo discloses all of the subject matter as described above except for specifically teaching that the primary digital signal is subject to adjacent channel interference filtering.

However, Spiegel discloses a similar method and apparatus for programming base band module, and further discloses a low pass filter to reject adjacent channel interference (abstract; figures 1, and 3; column 1, lines 25-32; column 4, lines 35-40).

Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to use the adjacent channel interference filter. One would have been motivated to combine the adjacent channel interference filter in Teo system in order to reject the interference which is generated when switching between different receiving modes.

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12. Claim 20 is rejected under 35 U.S.C. 103(a) as being unpatentable over Teo et al. (US 6,985,545) in view of Tsuie et al. (US 2004/0223449) further in view of Womack et al. (US 5,982,819) as applied to claim 19 above, and further in view of Li et al. (US 7,200,196).

Regarding Claim 20:

Teo discloses all of the subject matter as described above except for specifically teaching that the base band processing module comprises a Farrow interpolator.

However, Li discloses a method and apparatus for interpolation based timing recovery, and further discloses a Farrow interpolator for timing recovery (abstract; figures 1, 4, and 6; column 1, lines 25-32; column 4, lines 50-66; column 5, lines 1-50).

Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to use the Farrow interpolator in the Teo system. One would have been motivated to combine the Farrow interpolator for compensating the timing offsets.

Conclusion

13. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

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A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hirdepal Singh whose telephone number is 571-270-1688. The examiner can normally be reached on Mon-Fri (Alternate Friday Off) 8:00AM-5:00PM EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Shuwang Liu can be reached on 571-272-3036. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

HS

October 29, 2007



SHUWANG LIU
SUPERVISORY PATENT EXAMINER